



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KUSUMOTO
Serial No.: 09/773,502
Filed: February 2, 2001
Due Date: January 1, 2003
Title: SEMICONDUCTOR DEVICE HAVING A CONNECTION INSPECTING
CIRCUIT FOR INSPECTING CONNECTIONS OF POWER SOURCE
TERMINALS AND GROUNDING TERMINALS, AND INSPECTION
METHOD FOR THE SAME

Examiner: PATEL, PARESH H.
Group Art Unit: 2829
Docket: 10873.644US01
Confirmation No. 3298

9/a
Shackey
1-8-03

CERTIFICATE UNDER 37 CFR 1.10:

"Express Mail" mailing label number: EV 143560305 US
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I hereby certify that this paper or fee is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Commissioner for Patents, Washington, D.C. 20231.

By:
Name: John Junkers

AMENDMENT & RESPONSE

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed on August 1, 2002, please consider and enter the following amendments and remarks.

RECEIVED
JAN - 7 2003
TC 2800 MAIL ROOM

In the Claims

01/13/2003 SHACKLEY 0000 Please amend claims 20 and 44 as follows.

01 FC:1201 84.00 CH
02 FC:1202 324.00 CH

20. (Once Amended) A semiconductor device having a semiconductor integrated circuit, the semiconductor device comprising:

a first internal line provided within the semiconductor integrated circuit;

a first internal terminal, a second internal terminal, and a third internal terminal provided within the semiconductor integrated circuit;

a first external line provided outside the semiconductor integrated circuit;

01

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What

What should be constructive?